RAMTRON FRAM (FERROELECTRIC RANDOM ACCESS MEMORY) MEMORY

FRAM offers a unique set of features relative to other semiconductor technologies. Traditional mainstream semiconductor memories can be divided into two primary categories – volatile and nonvolatile. Volatile memories include SRAM (static random access memory) and DRAM (dynamic random access memory). SRAMs and DRAMs lose their contents after power is removed from the electronic system. RAM type devices are very easy to use, and are high performing, but they share the annoying quirk of losing their mind when the lights go out.

Non-volatile memories do not lose their contents when power is removed. However all of the mainstream non-volatile memories share a common ancestry that derives from ROM (read only memory) technology. As you might guess, something called read only memory is not easy to write, in fact it’s impossible. All of its descendants make it very difficult to write new information into them. They include technologies EPROM (almost obsolete now), EEPROM, and Flash. ROM based technologies are very slow to write, wear out after being written a small number of times, and use a large amount of power to write. FRAM offers features consistent with a RAM technology, but is non-volatile like a ROM technology. FRAM bridges the gap between the two categories and creates something completely new – a non-volatile RAM.

When an electric field is applied to a ferroelectric crystal, the central atom moves in the direction of the field. As the atom moves within the crystal, it passes through an energy barrier, causing a charge spike. Internal circuits sense the charge spike and set the memory. If the electric field is removed from the crystal, the central atom stays in position, preserving the state of the memory. Therefore, the FRAM memory needs no periodic refresh and when power fails, FRAM memory retains its data. It’s fast, and doesn’t wear out!

Serial FRAM

The 2-wire interface is a widely used Master, multi-slave protocol using a serial clock (SCL) and a serial data line (SDA). Multiple memory devices can reside on the 2-wire bus by using the device select pins A0-A2 on selected parts. The 2-wire protocol is designed for multi-drop applications as well.

Features:

- Industry standard protocol (2-wire & SPI)
- Low standby current
- Low power consumption
- Industry standard protocol (2-wire & SPI)
- High Endurance Read/Writes
- NoDelay™ Writes
- Direct hardware replacement for EEPROM
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Parallel FRAM

Nonvolatile Parallel high performance reads and writes with true nonvolatility without the battery. All bytewise memories have standard SRAM pinouts.

Features:

- Industry standard SRAM/EEPROM pinout
- Read and write at bus speed
- High Endurance
- Low power consumption, High Endurance

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This page of product is compliant.

Ramtron

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